

Electronic Acknowledgement Receipt

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| Application Number: | 10634899 |
| International Application Number: | |
| Confirmation Number: | 7340 |
| Title of Invention: | APPARATUS AND METHOD FOR PROTECTING CRITICAL RESOURCES AGAINST SOFT ERRORS IN HIGH PERFORMANCE MICROPROCESSOR |
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File Listing:

| Document Number | Document Description | File Name | File Size(Bytes)/Message Digest | Multi Part/.zip | Pages (if appl.) |
|-----------------|---|------------------------------------|--|-----------------|------------------|
| 1 | Request/Corrected Patent Application Publications | 02207_868702_Cert_Corr_9_30_09.pdf | 147409 e50b3c16c977a9ab87c9e7267ae9172b228za311 | no | 2 |

Warnings:

Information:

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|---|---|--|---|----|---|
| 2 | Request/Corrected Patent Application Publications | 02207_868702_Req_Cert_Correction_9_30_09.pdf | 70581 0430cd204463a4e9836508c1bf28eb8a3aa 1f704 | no | 2 |
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New International Application Filed with the USPTO as a Receiving Office

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